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In FIG. 5, three additional unnumbered transistors in the lower part of the figure have respective channel regions aligned along a second straight line. The second straight line is oblique to the straight line of the bent portion 90, forms a second acute angle with the longer axis direction 25 of the laser beam, and forms an obtuse angle with the first straight line. The first and second straight lines, with the straight line of the bent portion 90, form a zigzag pattern that is repeated, as indicated in FIGS. 21 and 22, described below. As shown in FIG. 5, the channel regions of the transistors do not overlap the bent portion 90 of the zigzag pattern. This arrangement prevents an electric field concentration generated by the interconnections at this bent portion 90 from affecting the interconnections of the driving transistors. When the channel portion overlaps the bent portion 90 where the electric field is concentrated, a malfunction may occur, and hence such an arrangement must be avoided. A projection distance A, projected with respect to the direction of laser beam scanning, of the shift pitch between the three driving transistors arranged on each of the first and second straight lines may be larger or smaller than the pitch P of the laser beam scanning. Typically, the channel width 34 is about 20  $\mu\text{m}$ , the channel length 35 is about 5  $\mu\text{m}$ , and the scan pitch P of the laser beam is about 15  $\mu\text{m}$ .

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In the second embodiment of the present invention, the direction of the width 34 of the channel region of each transistor is parallel to the direction 25 of the longer axis of the laser beam, as shown in FIG. 23. Referring to FIG. 23, the shift pitch A of three driving transistors arranged on a portion corresponding to one quarter of the wavelength, i.e., one quarter of the period of the repeating pattern, of the zigzag line should preferably be larger than the scan pitch P of the laser beam, although it may be smaller. In the second embodiment, the first and second straight lines respectively consist of a plurality of parallel straight line segments, parallel to the respective channel widths 34 of the respective transistors, and oblique line segments oblique to and connecting pairs of the straight line segments.

*IN THE CLAIMS:*

Please cancel claims 2, 4, 5, and 7 and replace the indicated claims with:

1. (Twice Amended) A liquid crystal display apparatus including:  
a liquid crystal display: and

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B  
a thin film transistor (TFT) panel, driving the liquid crystal display, and having a driving circuit area on which a plurality of driving transistors are located, said driving transistors including respective sources, gates, and drains, and said driving circuit area including gate interconnections interconnecting gates of at least pairs of said driving transistors, wherein

said gate interconnections are located along respective zigzag patterns, each zigzag pattern including a first straight line extending along a first direction, a second straight line extending along a second direction different from the first direction, and a third straight line intersecting and oblique to each of the first and second straight lines, and

said gates of said driving transistors that are interconnected are located on the first and second straight lines, and said channel regions of said driving transistors do not overlie the third straight line.

34  
3.  
(Twice Amended) The liquid crystal display apparatus according to claim 1, wherein each of the first and second straight lines includes a plurality of parallel straight line segments joined by respective oblique straight line segments oblique to the parallel straight line segments.

35  
6.  
(Twice Amended) The liquid crystal display apparatus according to claim 1, wherein said channel regions of said driving transistors that are interconnected have respective widths that are parallel to the first and second straight lines.

36  
8.  
(Twice Amended) The liquid crystal display apparatus according to claim 1, wherein said driver transistors are polycrystalline silicon, crystallized from amorphous silicon by irradiation with a laser beam tracing stripes on said TFT panel, the stripes being spaced at uniform interval on the TFT panel, and distance between a first of said driving transistors and a second of said driving transistors, neighboring and positioned nearest to the first driving transistor, is longer than the interval of the stripes that are traces of the laser beam.

9.  
(Twice Amended) The liquid crystal display apparatus according to claim 1, wherein said driver transistors are polycrystalline silicon, crystallized from amorphous silicon by irradiation with a laser beam tracing strips on said TFT panel, the strips being spaced at uniform interval on the TFT panel, and, in the channel region of each of said driving transistors, distance between a corner of the channel region nearest to said display area and a corner of the channel region farthest from said display area, is longer than the interval of the strips that are traces of the laser beam.

10. (Twice Amended) A thin film transistor panel for driving a liquid crystal display including a plurality of driving transistors located on a driving circuit area, said driving transistors including respective sources, gate, and drains, and said driving circuit including gate interconnections interconnecting gates of at least pairs of said driving transistors, wherein

said gate interconnections are located along respective zigzag patterns, each zigzag pattern including a first straight line extending along a first direction, a second straight line extending along a second direction different from the first direction, and a third straight line intersecting and oblique to each of the first and second straight lines, and

said gates of said driving transistors that are interconnected are located on the first and second straight lines, and said channel regions of said driving transistors do not overlie the third straight line.

Add the following claims:

11. (New) The liquid crystal display apparatus according to claim 10, wherein each of the first and second straight lines includes a plurality of parallel straight line segments joined by respective oblique straight line segments oblique to the parallel straight line segments.

12. (New) The liquid crystal display apparatus according to claim 10, wherein said channel regions of said driving transistors that are interconnected have respective widths that are parallel to the first and second straight lines.